

NOT RECOMMENDED FOR NEW DESIGN USE DMP2065UFDB



DMP2160UFDB

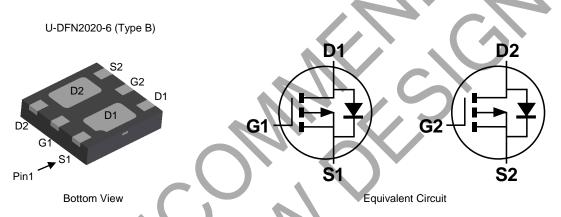
DUAL P-CHANNEL ENHANCEMENT MODE MOSFET

Features

- Low On-Resistance
 - $70m\Omega @V_{GS} = -4.5V$
 - 85mΩ @V_{GS} = -2.5V
 - 86mΩ (Typ) @V_{GS} = -1.8V
- Low Gate Threshold Voltage, -0.9V Max
- Fast Switching Speed
- Low Input/Output Leakage
- Low Profile, 0.5mm Max Height
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- An Automotive-Compliant Part is Available Under Separate Datasheet (<u>DMP2160UFDBQ</u>)

Mechanical Data

- Case: U-DFN2020-6 (Type B)
- Case Material: Molded Plastic, "Green" Molding Compound;
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish NiPdAu Annealed over Copper Leadframe;
 Solderable per MIL-STD-202, Method 208
- Weight: 0.0065 grams (Approximate)



Ordering Information (Note 4)

Part Number	Case	Packaging
DMP2160UFDB-7	U-DFN2020-6 (Type B)	3,000/Tape & Reel
DMP2160UFDB-7R	U-DFN2020-6 (Type B)	3,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

U-DFN2020-6 (Type B)



P2 = Marking Code YM = Date Marking Y = Year (ex: E = 2017) M = Month (ex: 9 = September) Dot Denotes Pin 1

Date Code Key

Year	2008	2009		2015	201	6 20	17 2	2018	2019	2020	2021	2022
Code	V	W		С	D	E	Ξ	F	G	Н	1	J
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



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Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V_{DSS}	-20	V
Gate-Source Voltage	V_{GSS}	±12	V
Drain Current (Note 5)	I _D	-3.8	Α
Pulsed Drain Current (Note 6)	I _{DM}	-13	А

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P_{D}	1.4	W
Thermal Resistance, Junction to Ambient	$R_{ heta JA}$	89	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

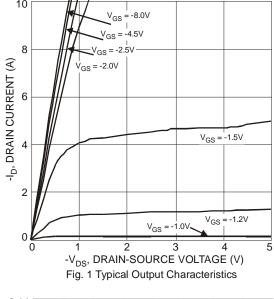
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

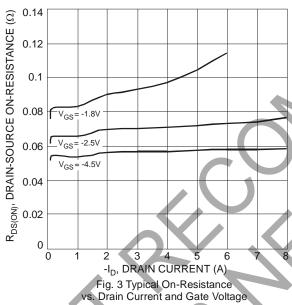
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Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	-20			>	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current	IDSS			-1	μΑ	$V_{DS} = -20V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}		1	±100 ±800	nA	$V_{GS} = \pm 8V, V_{DS} = 0V$ $V_{GS} = \pm 12V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	-0.45]	-0.9	V	$V_{DS} = V_{GS}, I_{D} = -250\mu A$
			54	70		$V_{GS} = -4.5V, I_D = -2.8A$
Static Drain-Source On-Resistance	R _{DS(ON)}	_	68	85	mΩ	$V_{GS} = -2.5V, I_D = -2.0A$
			86	_		$V_{GS} = -1.8V, I_D = -1.0A$
Forward Transfer Admittance	Yfs	-	8	_	S	$V_{DS} = -5V, I_{D} = -2.8A$
Diode Forward Voltage (Note 7)	V_{SD}		-0.7	-1.2	V	$V_{GS} = 0V, I_{S} = -1.6A$
DYNAMIC CHARACTERISTICS						
Input Capacitance	Ciss		536	_	pF	\/ 10\/ \/ 0\/
Output Capacitance	Coss	_	68	_	pF	$V_{DS} = -10V, V_{GS} = 0V$ f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	59		pF	1 – 1.000112
Gate Resistance	R_g		34		Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$
Total Gate Charge	Q_{g}	_	6.5		nC	\/ 4 <i>E</i> \/ \/ 40\/
Gate-Source Charge	Q_{gs}	_	8.0		nC	$V_{GS} = -4.5V, V_{DD} = -10V,$ $I_{D} = -1.5A$
Gate-Drain Charge	Q_{gd}	_	1.4		nC	ID = -1.3A
Turn-On Delay Time	t _{D(ON)}	_	11.51	_	ns	
Turn-On Rise Time	t _R	_	12.09	_	ns	$V_{GEN} = -4.5V, V_{DD} = -10V,$
Turn-Off Delay Time	t _{D(OFF)}	-	55.34	_	ns	$R_L = 10\Omega$, $R_G = 6\Omega$
Turn-Off Fall Time	t _F	_	27.54	_	ns	

Notes:

- 5. Device mounted on FR-4 PCB, on minimum recommended, 2oz Copper pad layout.6. Repetitive rating, pulse width limited by junction temperature.7. Short duration pulse test used to minimize self-heating effect.







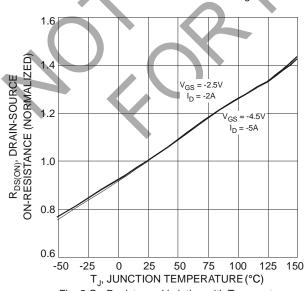
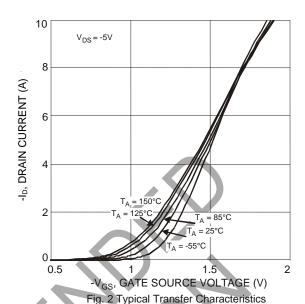
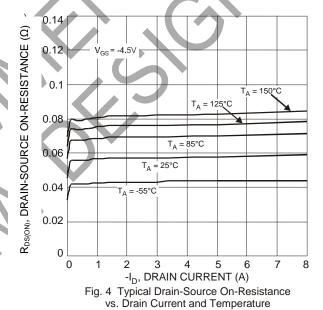
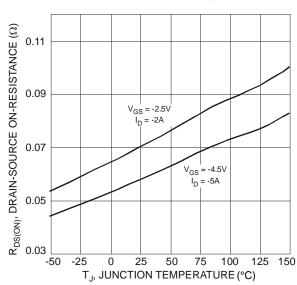


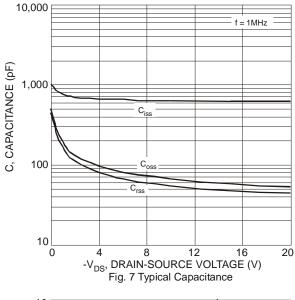
Fig. 5 On-Resistance Variation with Temperature

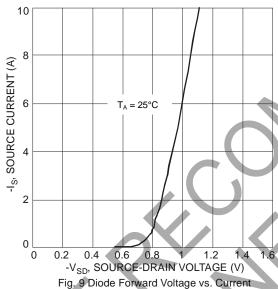


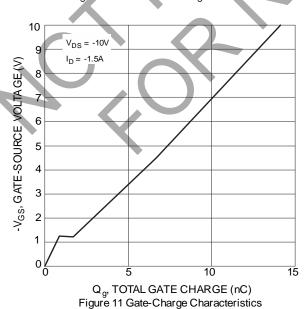


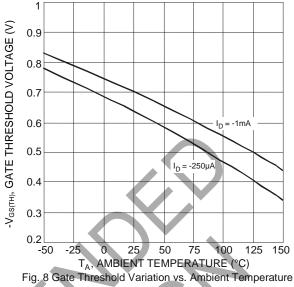












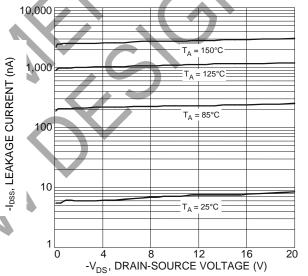
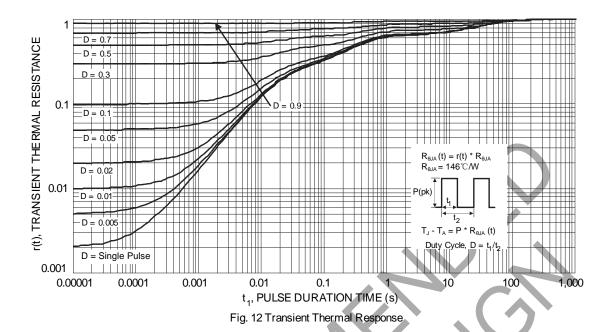


Fig. 10 Typical Drain-Source Leakage Current vs. Voltage



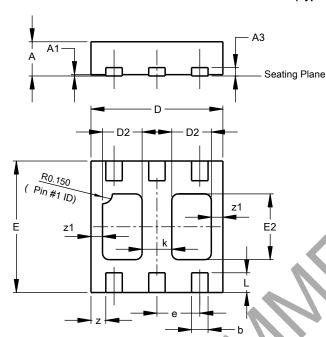




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type B)

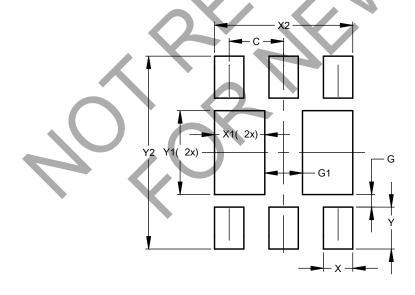


U-DFN2020-6						
(Type B)						
Dim	Min	Max	Тур			
Α	0.545	0.605	0.575			
A1	0.00	0.05	0.02			
A3	1	-	0.13			
b	0.20	0.30	0.25			
D	1.95	2.075	2.00			
D2	0.50	0.70	0.60			
е	-	-	0.65			
E	1.95	2.075	2.00			
E2	0.90	1.10	1.00			
k		, ,	0.45			
L	0.25	0.35	0.30			
z	-	-	0.225			
z1	-	-	0.175			
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type B)



Dimensions	Value		
Dillielisions	(in mm)		
С	0.650		
G	0.150		
G1	0.450		
Х	0.350		
X1	0.600		
X2	1.650		
Y	0.500		
Y1	1.000		
Y2	2.300		



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