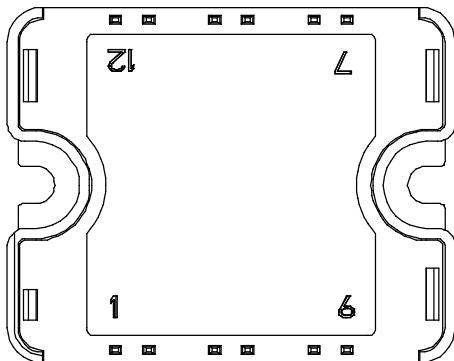
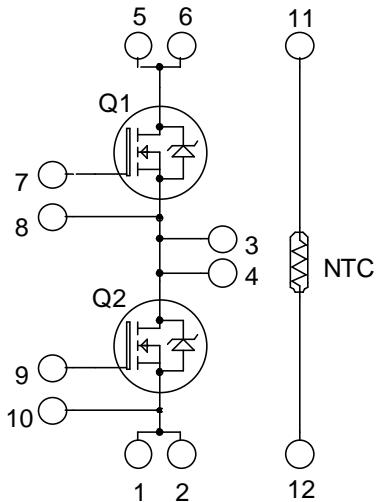


Phase leg
MOSFET Power Module

V_{DSS} = 600V
R_{DSon} = 230mΩ typ @ T_j = 25°C
I_D = 20A @ T_c = 25°C



Pins 1/2 ; 3/4 ; 5/6 must be shorted together

Application

- Welding converters
- Switched Mode Power Supplies
- Uninterruptible Power Supplies
- Motor control

Features

- Power MOS 8™ Ultrafast FREDFETs
 - Low R_{DSon}
 - Low input and Miller capacitance
 - Low gate charge
 - Ultrafast intrinsic reverse diode
 - Avalanche energy rated
 - Very rugged
- Very low stray inductance
 - Symmetrical design
- Internal thermistor for temperature monitoring
- High level of integration

Benefits

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Low profile
- RoHS Compliant

Absolute maximum ratings

Symbol	Parameter	Max ratings	Unit
V _{DSS}	Drain - Source Breakdown Voltage	600	V
I _D	Continuous Drain Current	T _c = 25°C	A
		T _c = 80°C	
I _{DM}	Pulsed Drain current	125	
V _{GS}	Gate - Source Voltage	±30	V
R _{DSon}	Drain - Source ON Resistance	276	mΩ
P _D	Maximum Power Dissipation	T _c = 25°C	W
I _{AR}	Avalanche current (repetitive and non repetitive)		A
		208	
		17	

 **CAUTION:** These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com

All ratings @ $T_j = 25^\circ\text{C}$ unless otherwise specified

Electrical Characteristics

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 600V V _{GS} = 0V	T _j = 25°C			250	μA
			T _j = 125°C			1000	
R _{DS(on)}	Drain – Source on Resistance	V _{GS} = 10V, I _D = 17A			230	276	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{GS} = V _{DS} , I _D = 1mA		3	4	5	V
I _{GSS}	Gate – Source Leakage Current	V _{GS} = ±30 V				±100	nA

Dynamic Characteristics

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
C _{iss}	Input Capacitance	V _{GS} = 0V V _{DS} = 25V f = 1MHz		5316			pF
C _{oss}	Output Capacitance			610			
C _{rss}	Reverse Transfer Capacitance			56			
Q _g	Total gate Charge	V _{GS} = 10V V _{Bus} = 300V I _D = 17A		165			nC
Q _{gs}	Gate – Source Charge			36			
Q _{gd}	Gate – Drain Charge			70			
T _{d(on)}	Turn-on Delay Time	Resistive switching @ 25°C V _{GS} = 15V V _{Bus} = 400V I _D = 17A R _G = 4.7Ω		37			ns
T _r	Rise Time			43			
T _{d(off)}	Turn-off Delay Time			115			
T _f	Fall Time			34			

Source - Drain diode ratings and characteristics

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit	
I _S	Continuous Source current (Body diode)		T _c = 25°C			20	A	
			T _c = 80°C			15		
V _{SD}	Diode Forward Voltage	V _{GS} = 0V, I _S = - 17A				1	V	
dv/dt	Peak Diode Recovery ①					30	V/ns	
t _{rr}	Reverse Recovery Time	I _S = - 17A V _R = 100V dI _S /dt = 100A/μs	T _j = 25°C			200	ns	
			T _j = 125°C			370		
Q _{rr}	Reverse Recovery Charge		T _j = 25°C		0.76		μC	
			T _j = 125°C		1.91			

① dv/dt numbers reflect the limitations of the circuit rather than the device itself.

 I_S ≤ - 17A di/dt ≤ 1000A/μs V_{DD} ≤ 400V T_j ≤ 125°C

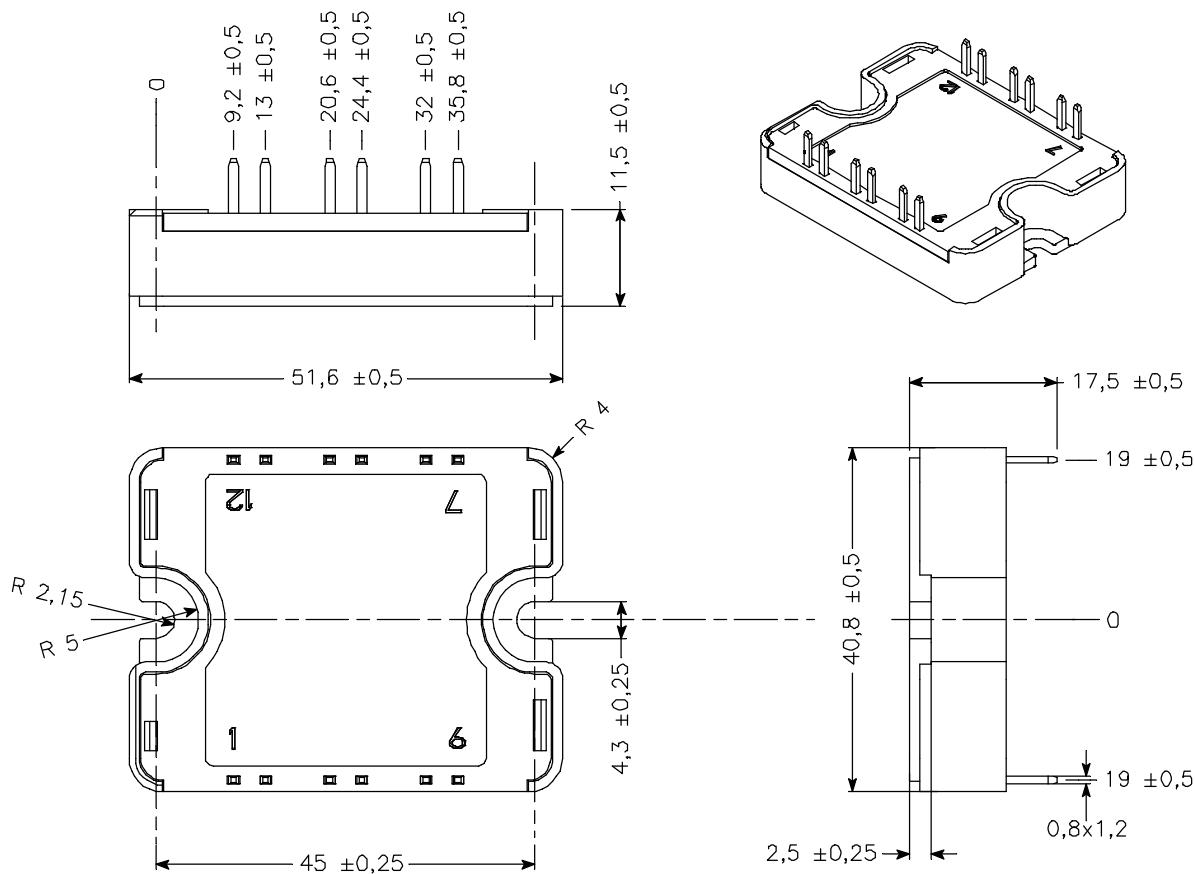
Thermal and package characteristics

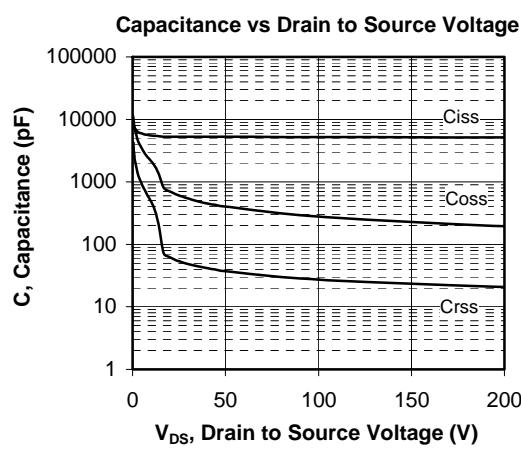
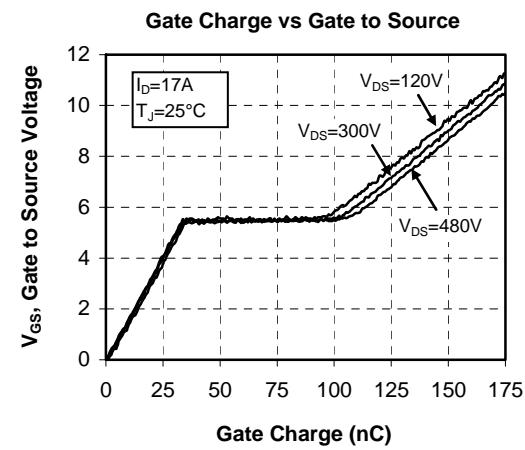
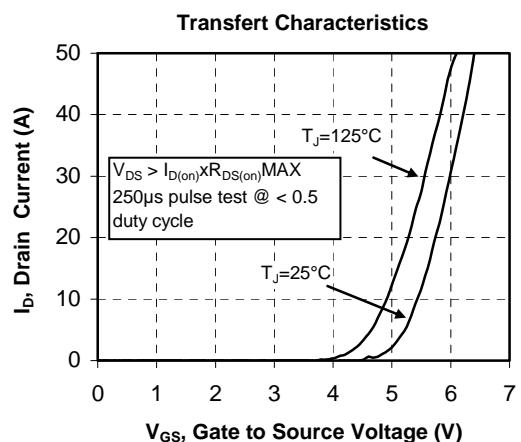
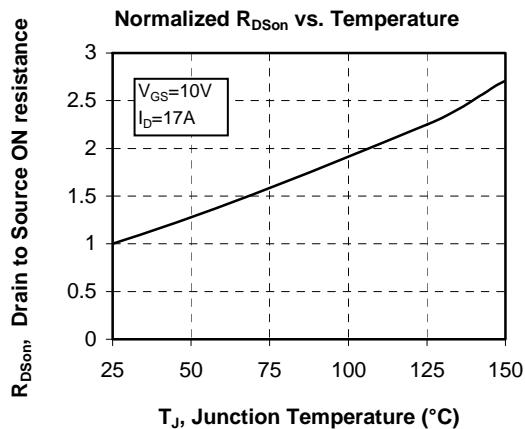
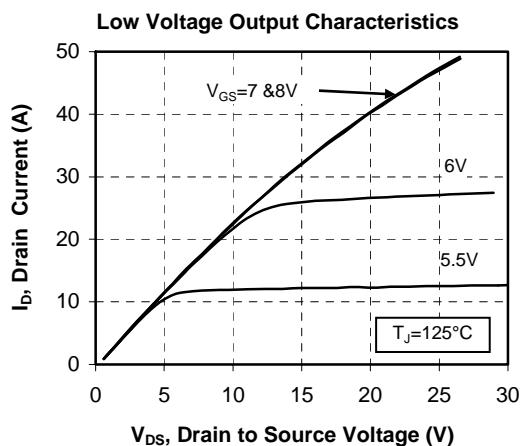
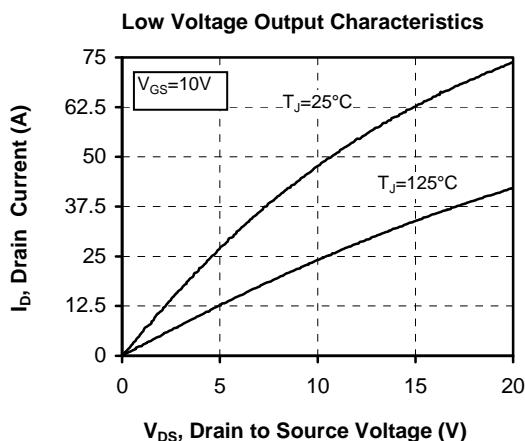
Symbol	Characteristic		Min	Typ	Max	Unit
R _{thJC}	Junction to Case Thermal Resistance			0.6	0.6	°C/W
V _{ISOL}	RMS Isolation Voltage, any terminal to case t =1 min, I isol<1mA, 50/60Hz		2500			V
T _J	Operating junction temperature range		-40		150	
T _{STG}	Storage Temperature Range		-40		125	°C
T _C	Operating Case Temperature		-40		100	
Torque	Mounting torque	To heatsink M4	2.5		4.7	N.m
Wt	Package Weight			80		g

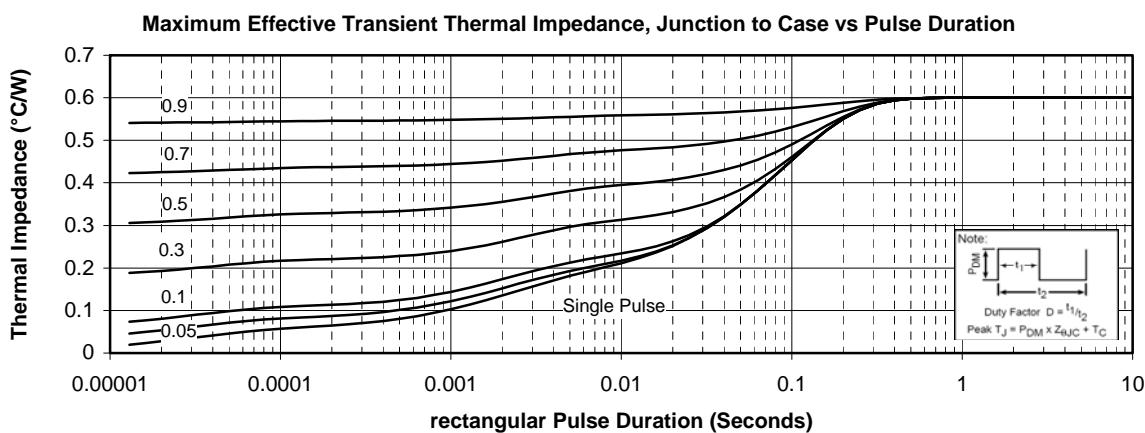
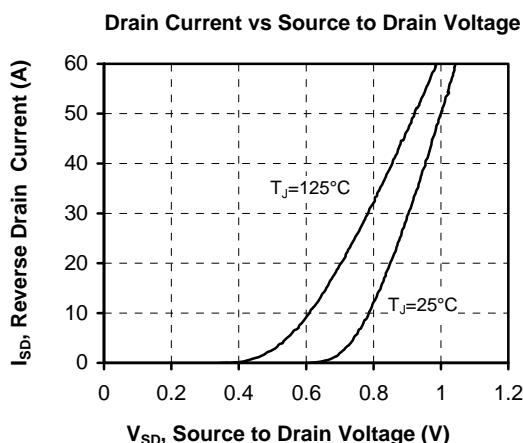
Temperature sensor NTC (see application note APT0406 on www.microsemi.com for more information).

Symbol	Characteristic		Min	Typ	Max	Unit
R ₂₅	Resistance @ 25°C			50		kΩ
B _{25/85}	T ₂₅ = 298.15 K			3952		K

$$R_T = \frac{R_{25}}{\exp\left[B_{25/85}\left(\frac{1}{T_{25}} - \frac{1}{T}\right)\right]} \quad \begin{array}{l} T: \text{ Thermistor temperature} \\ R_T: \text{ Thermistor value at } T \end{array}$$

SP1 Package outline (dimensions in mm)

 See application note 1904 - Mounting Instructions for SP1 Power Modules on www.microsemi.com

Typical Performance Curve




Microsemi reserves the right to change, without notice, the specifications and information contained herein

Microsemi's products are covered by one or more of U.S patents 4,895,810 5,045,903 5,089,434 5,182,234 5,019,522 5,262,336 6,503,786 5,256,583 4,748,103 5,283,202 5,231,474 5,434,095 5,528,058 and foreign patents. U.S and Foreign patents pending. All Rights Reserved.