

### PNP medium power transistor

Datasheet - production data

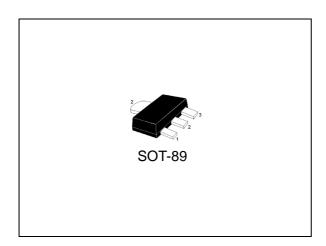
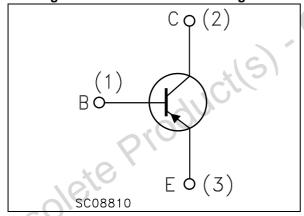


Figure 1. Internal schematic diagram



#### **Features**

- In compliance with the 2002/93/EC European directive
- · Available in tape & reel packing
- Surface mounting devices in medium power SOT-223 and SOT-89 packages

#### **Applications**

- Voltage regulation
- Relay driver
- Generic switch

#### **Description**

The STF826 is a PNP transistor manufactured using planar Technology resulting in rugged high performance devices.

**Table 1. Device summary** 

Order code	Marking	Package	Packaging
STF826	826	SOT-89	Tape and reel

Contents STF826

#### **Contents**

1	Absolute maximum ratings	3
2	Electrical characteristics	<b>4</b> 5
3	Package mechanical data	7
4	Packaging mechanical data	. 9
5	Revision history	. 11
0050	Electrical characteristics 2.1 Electrical characteristics (curves)  Package mechanical data  Packaging mechanical data  Revision history	



# 1 Absolute maximum ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V <sub>CBO</sub>	Collector-base voltage (I <sub>E</sub> = 0)	-60	V
V <sub>CEO</sub>	Collector-emitter voltage (I <sub>B</sub> = 0)	-30	V
V <sub>EBO</sub>	Collector-base voltage (I <sub>C</sub> = 0)	-5	V
I <sub>C</sub>	Collector current	-3	Α
I <sub>CM</sub>	Collector peak current (t <sub>P</sub> < 5 ms)	-6	Α
I <sub>B</sub>	Base current	-1	Α
I <sub>BM</sub>	Base peak current (t <sub>P</sub> < 5 ms)	-2	Α
P <sub>TOT</sub>	Total dissipation at T <sub>c</sub> = 25 °C	1.4	W
T <sub>STG</sub>	Storage temperature	-65 to 150	°C
T <sub>J</sub>	Max. operating junction temperature	150	°C

Table 3. Thermal data

Symbol	Parameter	Value	Unit
R <sub>thj-amb</sub> <sup>(1)</sup>	Thermal Resistance Junction-Amb max.	89	°C/W

<sup>1.</sup> Device mounted on a PCB area of 1 cm<sup>2</sup>.



Electrical characteristics STF826

### 2 Electrical characteristics

(T<sub>CASE</sub> = 25 °C; unless otherwise specified)

**Table 4. Electrical characteristics** 

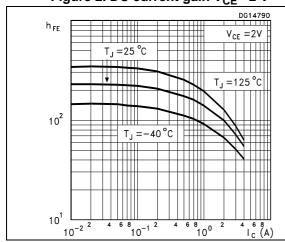
Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I <sub>CES</sub>	Collector cut-off current (V <sub>BE</sub> = 0)	V <sub>CE</sub> = -60 V			-10	μΑ
I <sub>CEO</sub>	Collector cut-off current (I <sub>B</sub> = 0)	V <sub>CE</sub> = -30 V			-100	μΑ
I <sub>EBO</sub>	Emitter cut-off current (I <sub>C</sub> = 0)	V <sub>EB</sub> = -5 V			-10	μА
V <sub>(BR)CBO</sub>	Collector-base breakdown voltage (I <sub>E</sub> = 0)	I <sub>C</sub> = -100 μA	-60	(	11/5	V
V <sub>(BR)CEO</sub> <sup>(1)</sup>	Collector-emitter breakdown voltage (I <sub>B</sub> = 0)	I <sub>C</sub> = -10 mA	-30	$Q_D$		V
V <sub>(BR)EBO</sub>	Collector-emitter breakdown voltage (I <sub>C</sub> = 0)	I <sub>E</sub> = -100 μA	-5			V
		$I_C = -1 \text{ A}, I_B = -50 \text{ mA}$			-0.4	V
V <sub>CE(sat)</sub> (1)	Collector-emitter saturation voltage	$I_C = -2 \text{ A}, I_B = -100 \text{ mA}$			-0.7	V
		$I_C = -3 \text{ A}, I_B = -150 \text{ mA}$			-1.1	V
V <sub>BE(sat)</sub> <sup>(1)</sup>	Base-emitter saturation voltage	$I_C = -2 \text{ A}, I_B = -100 \text{ mA}$			-1.2	V
		$I_C = -100 \text{ mA}, V_{CE} = -2 \text{ V}$	100			
h <sub>FE</sub>	DC current gain	I <sub>C</sub> = -1 A, V <sub>CE</sub> = -2 V	80		300	
_		$I_C = -3 \text{ A}, V_{CE} = -2 \text{ V}$	30			
f <sub>T</sub>	Transistor frequency	$V_{CE} = -10 \text{ V}, I_{c} = -0.1 \text{ A}$		100		MHz

Pulsed duration = 300 ms, duty cycle ≤1.5%.

#### 2.1 Electrical characteristics (curves)

Figure 2. DC current gain V<sub>CE</sub>= 2 V

Figure 3. DC current gain V<sub>CE</sub>= 5 V



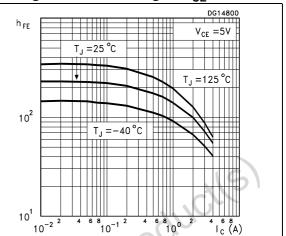
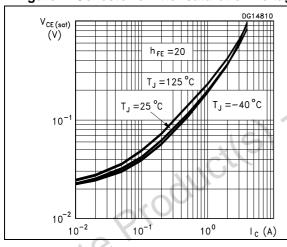


Figure 4. Collector-emitter saturation voltage

Figure 5. Base-emitter saturation voltage



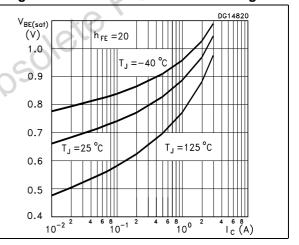
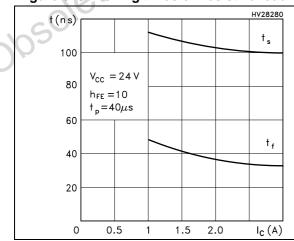
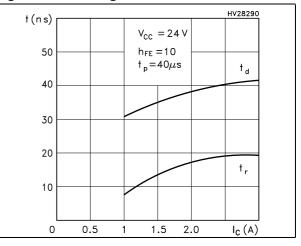


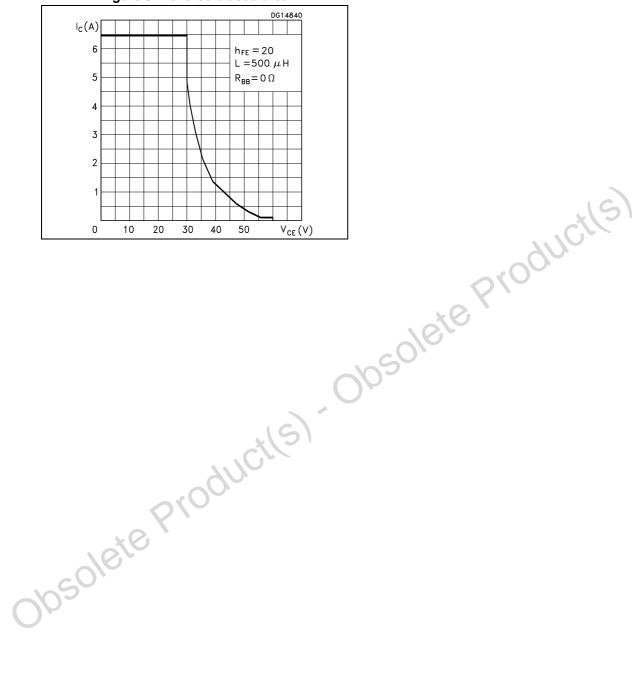
Figure 6. Switching times on resistive load OFF Figure 7. Switching times resistive on load ON





Electrical characteristics STF826

Figure 8. Reverse biased area



6/12 DocID11748 Rev 2

### 3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: <a href="https://www.st.com">www.st.com</a>. ECOPACK<sup>®</sup> is an ST trademark.

Table 5. SOT-89 mechanical data

	Dim		mm	
	Dim.	Min.	Тур.	Max.
	Α	1.40		1.60
	В	0.44		0.56
	B1	0.36		0.48
	С	0.35	210	0.44
	C1	0.35		0.44
	D	4.40	1010	4.60
	D1	1.62	Olo	1.83
	D3	-100	0.90	
	Е	2.29		2.60
	е	1.42		1.57
	e1	2.92		3.07
	н	3.94		4.25
	H1	2.70		3.10
	K	1°		8°
	L	0.89		1.20
١.0	R		0.25	
iosole	β		90°	



BOTTOM VIEW SIDE VIEW C1 B1(x2) D TOP VIEW 7098166\_REV\_E

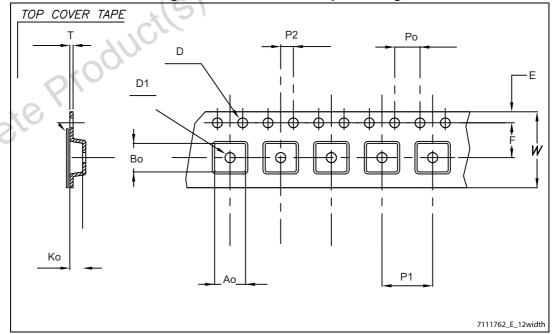
Figure 9. SOT-89 drawings

## 4 Packaging mechanical data

Table 6. SOT-89 carrier tape dimensions

Dim.	mm.		
Dilli.	Values	Tolerance	
Ao	4.91	± 0.10	
Во	4.52	± 0.10	
Ko	1.90	± 0.10	
F	5.50	± 0.10	
E	1.75	± 0.10	
W	12	± 0.30	
P2	2	± 0.10	
Po	4	± 0.10	
P1	8	± 0.10	
Т	0.30	± 0.10	
D	Ø 1.55	± 0.05	
D1	Ø 1.60	± 0.10	

Figure 10. SOT-89 carrier tape drawing



PIN 1: BASE PIN 2: COLLECTOR PIN 3: EMITTER Obsolete Products). Obsolete Products SOT-89 top view

Figure 11. SOT-89 package orientation in carrier tape

10/12 DocID11748 Rev 2

STF826 Revision history

### 5 Revision history

Table 7. Document revision history

Date	Revision	Changes
03-Aug-2005	1	Initial release.
25-Feb-2013	2	Removed part number STN826 in SOT-223 package.



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12/12 DocID11748 Rev 2

