

Features

- Built-In Bias Resistors Enable the Configuration of an Inverter Circuit Without Connecting External Input Resistors
- The Bias Resistors Consist of Thin-Film Resistors With Complete Isolation to Allow Negative Biasing of the Input. They Also Have the Advantage of Almost Completely Eliminating Parasitic Effects
- Only the On/Off Conditions Need to Be Set For Operation, Making Device Design Easy
- Halogen Free. "Green" Device (Note 1)
- · Moisture Sensitivity Level 1
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant.See Ordering Information)

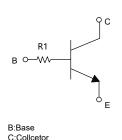
Maximum Ratings @ 25°C Unless Otherwise Specified

Parameter	Symbol	Value	Unit
Collector-Emitter Voltage	V_{CEO}	50	V
Collector-Base Voltage	V_{CBO}	50	V
Emitter-Base Voltage	V_{EBO}	5	V
Collector Current-Continuous	I _C	100	mA
Collector Dissipation	Pc	150	mW
Junction Temperature	T _J	150	°C
Storage Temperature Range	T _{STG}	-55 to +150	°C

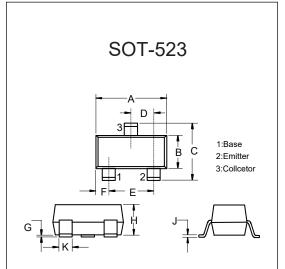
Note: 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

Device Marking: 03

Internal Structure

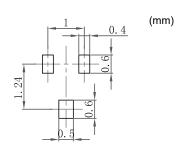


NPN Digital Transistor



DIMENSIONS					
DIM INCHES MIN MAX		M	M	NOTE	
		MIN MAX		NOTE	
Α	0.059	0.067	1.50	1.70	
В	0.030	0.033	0.75	0.85	
C	0.057	0.069	1.45	1.75	
D	0.020		0.50		TYP.
Е	0.035	0.043	0.90	1.10	
G	0.000	0.004	0.00	0.10	
Н	0.024	0.031	0.60	0.80	
J	0.004	0.008	0.10	0.20	
K	0.006	0.014	0.15	0.35	

Suggested Solder Pad Layout





Electrical Characteristics @ 25°C Unless Otherwise Specified

Parameter	Symbol	Min	Тур	Max	Units	Conditions
Collector-Base Breakdown Voltage	V _{(BR)CBO}	50			V	$I_{C}=50\mu A, I_{E}=0$
Collector-Emitter Breakdown Voltage	V _{(BR)CEO}	50			V	I _C =1mA, I _B =0
Emitter-Base Breakdown Voltage	$V_{(BR)EBO}$	5			٧	$I_{E}=50\mu A, I_{C}=0$
Collector Cut-off Current	I _{CBO}			0.5	μA	V_{CB} =50V, I_{E} =0
Emitter Cut-off Current	I _{EBO}			0.5	μA	V _{EB} =4V,I _C =0
DC Current Gain	h _{FE}	100	300	600		I _C =1mA, V _{CE} =5V
Collector-Emitter Saturation Voltage	V _{CE(sat)}			0.3	V	I _C =5mA, I _B =0.25mA
Input Resistance	R ₁	3.29	4.7	6.11	ΚΩ	
Transition Frequency	f _T		250		MHz	V _{CE} =10.0V, I _E =-5mA, f=100MHz



Curve Characteristics

Fig. 2 - DC Current Gain Characteristics

Common Emitter
V_{CE}=5V

400

T_A=100°C

100

0.1

1 1 10 100

Collector Current (mA)

Fig. 3 - Collector-Emitter Saturation Voltage
Characteristics

1000

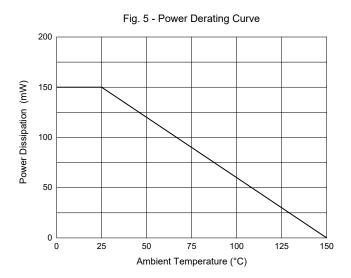
T_A=100°C

T_A=25°C

100

Collector Current (mA)

Collector-Emitter Voltage (V)





Ordering Information

Device	Packing
Part Number-TP	Tape&Reel:3Kpcs/Reel

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